

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S45	17	((("3905023") or ("4410938") or ("4412285") or ("4792955") or ("4819234") or ("4982402") or ("5119377") or ("5125087") or ("5214652") or ("5437039") or ("5533192") or ("5706407") or ("5792955") or ("5812425") or ("5937203") or ("6016548") or ("6202174"))).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/11/13 18:14
S47	8	((("5398196") or ("5561761") or ("6711660") or ("6711700") or ("6839892") or ("6845470") or ("6961806") or ("7024581"))).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/11/13 20:03
S46	219	714/30,34,35,40.ccls. and ((edit\$3 correct\$3 alter\$3 chang\$3 debug\$4 modify\$3) with (cpu processor microprocessor "processing unit") near3 (state register))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/11/14 17:21
S49	267	712/227,228.ccls. and ((edit\$3 correct\$3 alter\$3 chang\$3 debug\$4 modify\$3) with (cpu processor microprocessor "processing unit") near3 (state register))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/11/14 17:22
S10	539	(chang\$3 alter\$3 edit\$3 correct\$3 modify\$3) near3 registers same (second\$3 supplement\$3 auxiliary support\$3 servic\$3 alternat\$3 co) near3 (processor cpu microprocessor controller microcontroller "processing unit")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/11/15 20:15
S52	568	(chang\$3 alter\$3 edit\$3 correct\$3 modify\$3) near3 registers same (second\$3 supplement\$3 auxiliary support\$3 servic\$3 alternat\$3 co) near3 (processor cpu microprocessor controller microcontroller "processing unit")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/11/15 20:17
S54	35	S52 not (@pd<"20061012")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/11/15 20:25
S56	70	714/9,10,28,30,31,34,38.ccls. and (validat\$3 check\$3 verify\$3 examin\$3 correct\$3 modify\$3 alter\$3 fix\$3) near3 (cpu processor microprocessor) near1 state	USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/11/15 20:33

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S57	168	714/34.ccls. and (partner\$3 pair\$3 multiple second\$3 supplement\$3 auxiliary support\$3 servic\$3 alternat\$3 co) near3 (processor cpu microprocessor controller microcontroller "processing unit")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/11/15 20:34
S58	137	714/9,10,28,30,31,34,38.ccls. and (register processor near1 state) near3 (dump\$3 export\$3 copy\$3 stor\$3 read\$3 output\$3) same (second\$3 supplement\$3 auxiliary support\$3 servic\$3 alternat\$3 co) near3 (processor cpu microprocessor controller microcontroller "processing unit")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/11/15 20:44
S59	442	(chang\$3 alter\$3 edit\$3 correct\$3 modify\$3) near3 registers same (second\$3 supplement\$3 auxiliary support\$3 servic\$3 alternat\$3 co) near3 (processor cpu microprocessor controller microcontroller "processing unit")	USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/11/15 20:49
S60	18	((chang\$3 alter\$3 edit\$3 correct\$3 modify\$3) near3 registers same (second\$3 supplement\$3 auxiliary support\$3 servic\$3 alternat\$3 co) near3 (processor cpu microprocessor controller microcontroller "processing unit")) not (@pd<"20061012")	USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/11/15 20:55
S61	2	((debug\$4 fault fail\$3 error) and ("processing unit" processor computer cpu) and (allocat\$3) and (verify\$3 confirm\$3 check\$3) near6 (halt\$3 stop\$4 paus\$3 stall\$3) and (register "processor state" "state processor")).clm.	US-PGPUB	OR	ON	2007/11/15 20:59
S55	84	714/38.ccls. and registers near2 (sav\$3 stor\$3 load\$3) same (chang\$3 alter\$3 edit\$3 correct\$3 modify\$3 fix\$3 repair\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/11/15 21:34
L6	127	714/37.ccls. and (registers (state near1 processor)) near3 (sav\$3 stor\$3 load\$3 chang\$3 alter\$3 edit\$3 correct\$3 modify\$3 fix\$3 repair\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2007/11/15 21:38

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Thu, 15 Nov 2007, 9:07:17 PM EST

Search Query Display

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Results

#1 (((register or ~processor state~ or ~processing state~ or
~state of the processor~) and (debug or debugging or fix or
fixing or correcting or correct or repair or repairing or modify or
modifying) and (second or secondary or service or alternate or
~debug processor~))<in>metadata)

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